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PATENT APPLICATION

INVENTOR(S)

Dale E. Parson

Bryan Schlieder

James C. Vollmer Jay Patrick Wilshire "Express Mail" Label No. EK98L 291 995 & S
I hereby certify that this paper or fee is being deposited with the
United States Postal Service "Express Mail Post Office to
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Washington, D.C. 20231.

Date of Deposit: May 30, 2000

Signature: Leresa M. Hanlie

JC835 U.S. PTO 09/583057

CASE

3-2-1-4

TITLE

Control Method and Apparatus for Testing of Multiple Processor Integrated Circuits and Other Digital Systems

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

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Enclosed are the following papers relating to the above-named application for patent:

Specification

4 Sheets of informal drawing(s)

1 Assignment with Cover Sheet

Declaration and Power of Attorney

Information Disclosure Statement with Form PTO-1449 and cited reference(s)

CLAIMS AS FILED				
	NO. FILED	NO. EXTRA	RATE	CALCULATIONS
Total Claims	20-20 =	0	x \$18 =	\$0
Independent Claims	5-3 =	2	x \$78 =	\$156
Multiple Dependent Claim(s), if applicable			\$260 =	\$0
Basic Fee				\$690
,		·	TOTAL FEE:	\$846

Please file the application and charge Lucent Technologies Deposit Account No. 12-2325 the amount of \$846, to cover the filing fee. Duplicate copies of this letter are enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Deposit Account No. 12-2325 as required to correct the error.

Please address all correspondence to: Joseph B. Ryan, Ryan & Mason, L.L.P., 90 Forest Avenue, Locust Valley, New York 11560. Telephone calls should be made to the under-signed attorney at (516) 759-7517.

Respectfully,

Joseph B. Ryan

Reg. No. 37,922

Date: May 30, 2000 Attorney for Applicant(s)

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